

Fig 1.

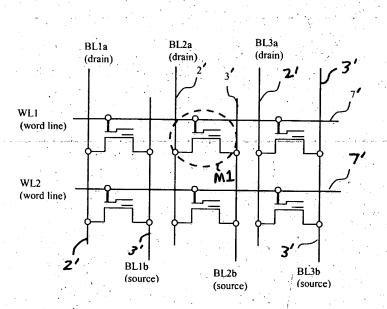


Fig 2.

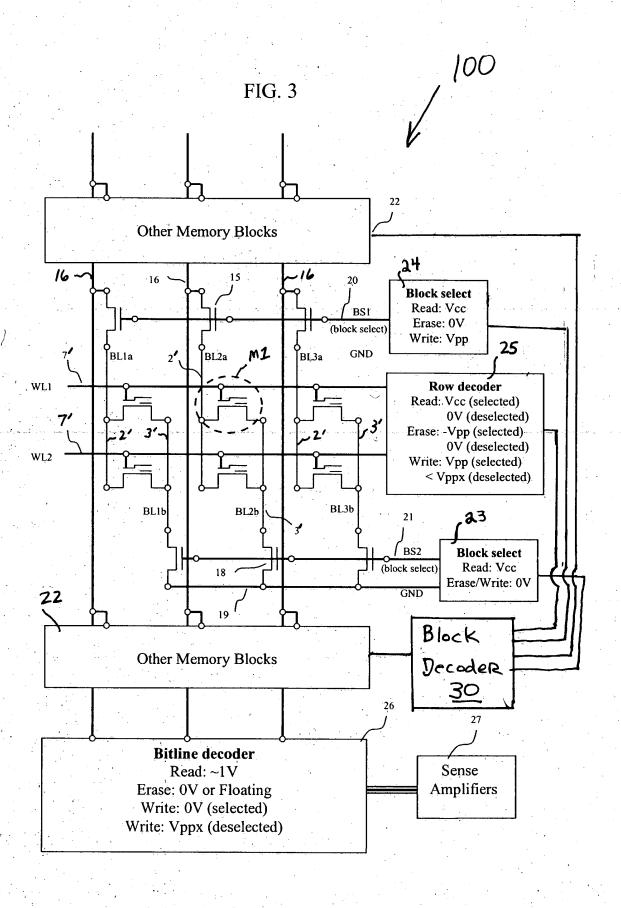
Table 1

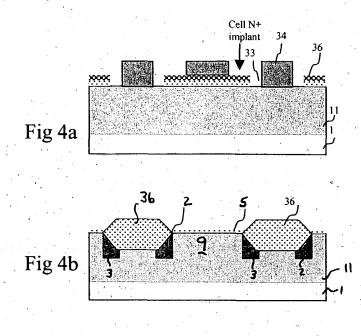
BL1a	BL1b	BL2a	BL2b	BL3a	BL3b	Wi.i	WL2
F	F	F	F	F	F		0
Vppx	F	0	F	Vnnx	F	+ PP	Vnny
Vr	0	Vr	0	Vr	0	 	<vppx< td=""></vppx<>
	F	F F	F F F Vppx F 0	F F F F Vppx F 0 F	F F F F Vppx F 0 F Vppx	F F F F F Vppx F 0 F Vppx F	F F F F F F F Vpp Vppx F 0 F Vppx F Vpp

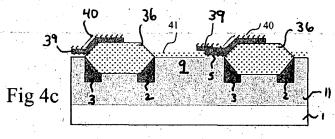
Note: Vppx <Vpp:2, Vr~1V

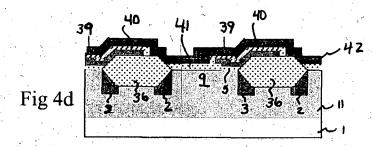
Fig. 2a

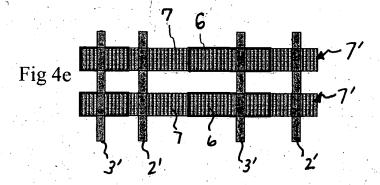
100

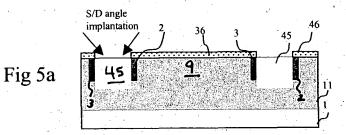


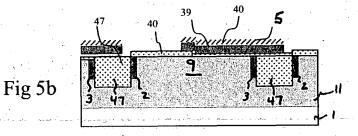


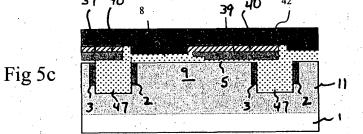


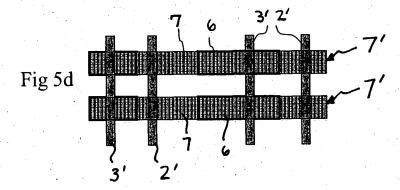












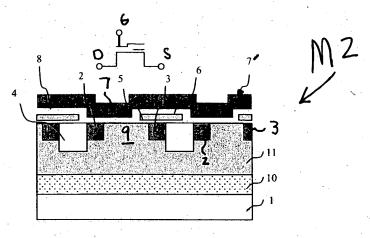


Fig. 6

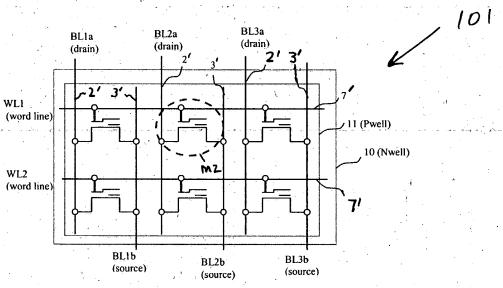


Fig. 7

Table 2

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	BLla	BL1b	BL2a	BL2b	BL3a	BL3b	WL1	WL2	Pwell	Nwell
Erase	F	F	F	F	F	F.	0	Vpp	Vpp	Vpp
Write	Vppx	F	0.	F	Vppx	F	Vpp	<vppx< td=""><td>0</td><td>0</td></vppx<>	0	0
Read	Vr	0	Vr	0	Vr	0	~Vcc	0	0	0

Note: Vppx <=Vpp:2, Vr~1V

Fig. 7a

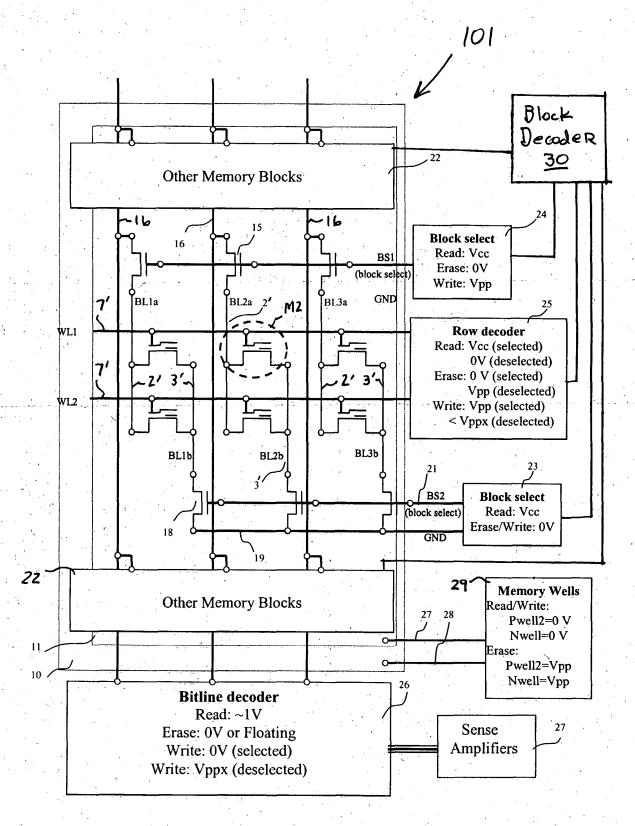
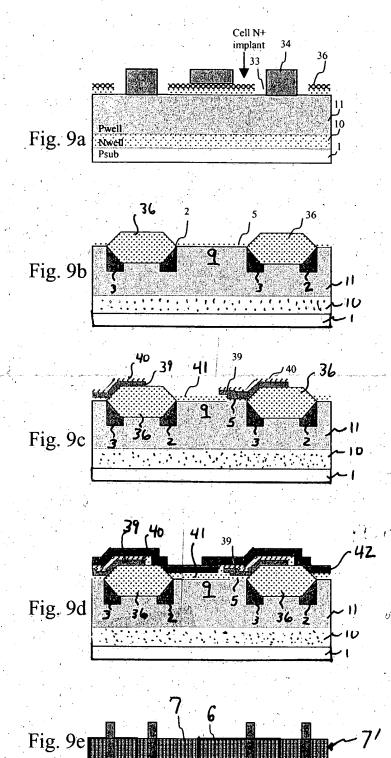


Fig. 8



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